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DESCRIPTION

DATA REPRODUCTION APPARATUS, VIDEO DISPLAY APPARATUS
AND SOFTWARE UPDATING SYSTEM AND SOFTWARE UPDATING METHOD
5 USING THE SAME

Technical Field

The present invention relates to a data reproduction
apparatus, a video display apparatus, and a software updating
10 system and a software updating method using the data
reproduction apparatus and the video display apparatus.

Background Art

When problems occur in software for a television
15 receiver, it is necessary to update the software in order to
correct the problems. For this reason, a software updating
circuit is suggested for updating software for a television
receiver. For example, a software updating circuit described
in JP 10-164446 A is provided with a changeover switch that
20 enables software to be changed in a market adjustment mode
via a variety of input/output terminals in a television
receiver.

Fig. 7 is a block diagram showing a conventional
software updating circuit. The software updating circuit in
25 Fig. 7 comprises a flash ROM (Read Only Memory) incorporating

microprocessor 101, a switching circuit 103, and a signal processor 104.

Software 109 is written in the flash ROM in the microprocessor 101. The microprocessor 101 outputs a
5 clock/data line switching signal 102 to the switching circuit 103. The switching circuit 103 feeds a clock/date signal 108 for updating the software to the microprocessor 101.

The television receiver has an S video input terminal 105. The S video input terminal 105 includes a luminance
10 signal input terminal 106 and a chrominance signal input terminal 107. The S video input terminal 105 is connected with a board or a personal computer 110 for updating the software.

In the example of Fig. 7, when the television receiver
15 enters a market adjustment mode, update software is input from the software updating board or the personal computer 110 by way of the luminance signal input terminal 106, the chrominance signal input terminal 107, and the switching circuit 103 to the microprocessor 101 as the clock/data signal
20 108.

In this manner, the software 109 for the microprocessor 101 is updated by the clock/data signal 108 from the software updating board or the personal computer 110.

With the conventional software updating circuit,
25 however, a service man has to visit a house to set the

television receiver in a market adjustment mode before connecting a special software updating board or the personal computer 110 to the S video input terminal 105 of the television receiver.

5 Further, even if such conditions for software updating are satisfied, the operation of software updating is so complicated that a user cannot update software on his or her own at home without a service man visiting his or her house.

10 Disclosure of Invention

It is an object of the invention to provide a data reproduction apparatus that allows a user to easily update software for a video display apparatus.

Another object of the invention is to provide a video
15 display apparatus that allows the user to easily update software.

Still another object of the invention is to provide a software updating system and a software updating method that allow the user to easily update software for a video display
20 apparatus.

A data reproduction apparatus according to one aspect of the invention, for reproducing data recorded in a recording medium, and capable of being connected via an interface bus to a video display apparatus that operates according to
25 software, comprises a transmitter that transmits video data

and audio data read from the recording medium, using a first area and a second area, respectively, to the video display apparatus via the interface bus, and a controller that controls the transmitter to transmit, at the time of a software update for the video display apparatus, update software read from the recording medium to the video display apparatus via the interface bus, using a third area for transmission of additional data that is different from the first and second areas, while indicating the software update to the video display apparatus.

The video data and the audio data read from the recording medium are transmitted, using the first area and the second area, respectively, from the transmitter in the data reproduction apparatus via the interface bus to the video display apparatus. This causes an image to be displayed on the video display apparatus based on the video data, and a sound to be output from the video display apparatus based on the audio data.

At the time of the software update for the video display apparatus, the controller controls the transmitter to transmit the update software read from the recording medium to the video display apparatus via the interface bus, using the third area for transmission of the additional data. In addition, the controller indicates the software update for

the video display apparatus. The software for the video display apparatus is thus updated using the update software.

This allows the user to easily update the software for the video display apparatus using the data reproduction
5 apparatus.

Information related to the software update may further be recorded in the recording medium, and the controller may control, at the time of the software update for the video display apparatus, the transmitter to transmit the related
10 information read from the recording medium to the video display apparatus via the interface bus, using the first area and/or the second area.

In this case, at the time of the software update for the video display apparatus, the related information read from
15 the recording medium is transmitted to the video display apparatus via the interface bus, using the first area and/or the second area. This allows the information related to the software update to be displayed or output on or from the video display apparatus.

20 The related information may include audio data, and the controller may control the transmitter, at the time of the software update for the video display apparatus, to transmit the audio data in the related information read from the recording medium to the video display apparatus via the
25 interface bus, using the second area.

In this case, at the time of the software update for the video display apparatus, the audio data in the related information read from the recording medium is transmitted to the video display apparatus via the interface bus, using the
5 second area. This allows the information related to the software update to be output from the video display apparatus as a sound.

The related information may include video data, and the controller may control the transmitter, at the time of the
10 software update for the video display apparatus, to transmit the video data in the related information read from the recording medium to the video display apparatus via the interface bus, using the first area.

In this case, at the time of the software update for the
15 video display apparatus, the video data in the related information read from the recording medium is transmitted to the video display apparatus via the interface bus, using the first area. This allows the information related to the software update to be displayed on the video display apparatus
20 as an image.

The related information may include operational guide information representing an operational procedure for the software update.

In this case, at the time of the software update for the
25 video display apparatus, the operational guide information

representing the operational procedure for the software update is transmitted to the video display apparatus via the interface bus, using the first area and/or the second area. This allows the operational procedure for the software update
5 to be displayed or output on or from the video display apparatus.

The interface bus may include a data line that transmits the video data, the audio data, and the additional data, a clock line that transmits a clock signal, and a control line
10 that transmits a control signal, wherein the first area may be a video period of the video data, and the second and third areas may be present in a blanking interval of the video data, and the transmitter may output a clock signal to the clock line while transmitting, in synchronization with the clock
15 signal, the update software via the data line using the third area.

In this case, the video data, audio data, and additional data read from the recording medium are transmitted to the video display apparatus via the data line, the clock signal
20 is transmitted via the clock line, and the control signal is transmitted via the control line. The update software is transmitted in synchronization with the clock signal, using the third area for the additional data within a blanking interval of the video data. This allows the software for the
25 video display apparatus to be updated using the interface bus

that connects between the data reproduction apparatus and the video display apparatus.

The update software may include identification information for identifying an object whose software should be updated, and the controller may indicate a software update to the video display apparatus based on the identification information.

This provides an indication of the object whose software should be updated in the video display apparatus based on the identification information.

The data reproduction apparatus may further comprise a storage device that stores software for the controller, wherein the controller may indicate a software update to the video display apparatus when the identification information represents the video display apparatus, and may update the software stored in the storage device, using the update software read from the recording medium, when the identification information represents a software update for the controller.

In this case, when the identification information represents the video display apparatus, the controller indicates the software update to the video display apparatus. When the identification information represents the software update for the controller, the controller updates the software stored in the storage device, using the update

software read from the recording medium. This allows the software for the video display apparatus and the software for the controller in the data reproduction apparatus to be selectively updated.

5 A video display apparatus according to another aspect of the invention comprises a video display apparatus that operates according to software, and a data reproduction apparatus for reproducing data recorded in a recording medium, and capable of being connected to the video display
10 apparatus via an interface bus, wherein the data reproduction apparatus includes a transmitter that transmits video data and audio data read from the recording medium, using a first area and a second area, respectively, to the video display apparatus via the interface bus, and a controller that
15 controls the transmitter to transmit, at the time of a software update for the video display apparatus, update software read from the recording medium to the video display apparatus via the interface bus, using a third area for transmission of additional data that is different from the
20 first and second areas, while indicating the software update to the video display apparatus, and wherein the video display apparatus comprises a receiver that receives the video data and the audio data transmitted by the data reproduction apparatus via the interface bus using the first area and the
25 second area, respectively, an audio output unit that outputs

a sound based on the audio data received by the receiver, a video display unit that displays an image based on the video data received by the receiver, a processor that controls the video display unit and the audio output unit, and a storage
5 that stores software for the processor, wherein the processor updates, upon reception of the update software by the receiver that is transmitted via the interface bus using the third area for transmission of the additional data that is different from the first and second areas, and indication of the software
10 update by the data reproduction apparatus, the software stored in the storage using the update software received by the receiver.

The video data and the audio data transmitted by the data reproduction apparatus, using the first area and the second
15 area, respectively, are received by the receiver in the video display apparatus via the interface bus. The sound is output from the audio output unit based on the audio data received by the receiver, and the image is displayed on the video display unit based on the video data received by the receiver.

20 Moreover, when the update software has been received by the receiver via the interface bus using the third area for transmission of the additional data, and the software update has been indicated by the data reproduction apparatus, the software stored in the storage device is updated using the
25 received update software. In this way, the software for the

video display apparatus can be easily updated using the data reproduction apparatus.

The processor may control the receiver to receive information related to the software update that is
5 transmitted via the interface bus using the first area and/or the second area, and cause the video display unit to display an image and the audio output unit to output a sound, based on the related information received by the receiver.

In this case, the information related to the software
10 update that is transmitted via the interface bus using the first area and/or the second area is received by the receiver. The image is displayed by the video display unit while the sound is output by the audio output unit, based on the received related information. This allows the user to easily recognize
15 the information related to the software update visually and audibly.

The video display apparatus may further comprise an expansion function unit that implements an expansion
20 function, another processor that controls the expansion function unit, and another storage that stores software for the processor for the expansion function, wherein the processor may transmit, upon indication of a software update for the other processor by the data reproduction apparatus, the update software received by the receiver to the other
25 processor while indicating the software update for the other

processor, and the other processor may update the software stored in the other storage according to the indication by the processor, using the update software transmitted from the processor.

5 In this case, when the software update for the other processor in the video display apparatus has been indicated by the data reproduction apparatus, the received update software is transmitted to the other processor, and the software update is indicated to the other processor. This
10 causes the other processor to update the software stored in the other storage using the update software. In this way, the software for the plurality of processors in the video display apparatus can be easily updated using the data reproduction apparatus.

15 The video display apparatus may include a television receiver. In this case, the software for the television receiver can be easily updated by the data reproduction apparatus.

20 A software updating system according to still another aspect of the invention comprises a video display apparatus that operates according to software, and a data reproduction apparatus for reproducing data recorded in a recording medium, and capable of being connected to the video display apparatus via an interface bus, wherein the data reproduction
25 apparatus includes a transmitter that transmits video data

and audio data read from the recording medium, using a first area and a second area, respectively, to the video display apparatus via the interface bus, and a controller that controls the transmitter to transmit, at the time of a software update for the video display apparatus, update software read from the recording medium to the video display apparatus via the interface bus, using the third area for transmission of the additional data that is different from the first and second areas, while indicating the software update to the video display apparatus, and wherein the video display apparatus comprises a receiver that receives the video data and the audio data transmitted by the data reproduction apparatus via the interface bus, using the first area and the second area, respectively, an audio output unit that outputs a sound based on the audio data received by the receiver, a video display unit that displays an image based on the video data received by the receiver, a processor that controls the video display unit and the audio output unit, and a storage that stores software for the processor, wherein the processor updates, upon reception of the update software by the receiver that is transmitted via the interface bus using the third area for transmission of the additional data, and indication of the software update by the data reproduction apparatus, the software stored in the storage using the update software received by the receiver.

The video data and the audio data read from the recording medium are transmitted by the transmitter in the data reproduction apparatus, using the first area and the second area, respectively, to the video display apparatus via the interface bus. The video data and the audio data transmitted by the data reproduction apparatus, using the first area and the second area, respectively, are thus received by the receiver in the video display apparatus via the interface bus. The sound is output from the audio output unit based on the audio data received by the receiver, and the image is displayed on the video display unit based on the video data received by the receiver.

At the time of the software update for the video display apparatus, the controller controls the transmitter to transmit the update software read from the recording medium to the video display apparatus via the interface bus, using the third area for transmission of the additional data. In addition, the controller indicates the software update to the video display apparatus. The software stored in the storage device in the video display apparatus is thus updated using the received update software.

In this way, the software for the video display apparatus can be easily updated using the data reproduction apparatus.

A software updating method according to yet another aspect of the invention, for updating software for a video display apparatus using a data reproduction apparatus for reproducing data recorded in a recording medium, and capable
5 of being connected to the video display apparatus via an interface bus, comprises the steps of transmitting, at the time of a software update for the video display apparatus, update software read by the data reproduction apparatus from the recording medium to the video display apparatus via the
10 interface bus, using a third area for transmission of additional data other than a first area for transmission of video data and a second area for transmission of audio data; indicating the software update to the video display apparatus by the data reproduction apparatus; receiving by the video
15 display apparatus, upon indication of the software update by the data reproduction apparatus to the video display apparatus, the update software transmitted via the interface bus using the third area; and updating the software for the video display apparatus using the received update software.

20 In the software updating method, at the time of the software update for the video display apparatus, the update software read by the data reproduction apparatus from the recording medium is transmitted to the video display apparatus via the interface bus, using the third area other
25 than the first area for transmission of the video data and

the second area for transmission of the audio data. When the software update has been indicated by the data reproduction apparatus to the video display apparatus, the update software transmitted via the interface bus using the third area is
5 received by the video display apparatus. The software for the video display apparatus is updated using the received update software.

In this way, the software for the video display apparatus can be easily updated using the data reproduction
10 apparatus.

Brief Description of Drawings

Fig. 1 is a block diagram showing the configuration of a software updating system according to an embodiment of the
15 invention;

Fig. 2 is a flowchart showing processing by a microprocessor in the DVD reproduction apparatus in Fig. 1;

Fig. 3 is a flowchart showing the processing by the microprocessor in the DVD reproduction apparatus in Fig. 1;

20 Fig. 4 is a flowchart showing processing by a microprocessor in the television receiver in Fig. 1;

Fig. 5 is a flowchart showing the processing by the microprocessor in the television receiver in Fig. 1;

Fig. 6 is a flowchart showing processing by a
25 microprocessor in the television receiver in Fig. 1; and

Fig. 7 is a block diagram showing the configuration of a conventional software updating circuit.

Best Mode for Carrying Out the Invention

5 Fig. 1 is a block diagram showing the configuration of a software updating system according to an embodiment of the present invention.

As shown in Fig. 1, the software updating system comprises a DVD (Digital Versatile Disk) reproduction
10 apparatus 200 and a television receiver 300. The television receiver 300 is connected with the DVD reproduction apparatus 200. A CD-ROM (Compact-Disk Random-Access Memory) 201 has recorded therein update software and operational guide information in MPEG (Moving Picture Coding Experts Group)
15 form.

The update software herein includes software for correcting problems in an earlier version of software, software for reinforcing or adding functions, a later version of software, and the like. The operational guide information
20 is information for guiding visually and audibly a user through the operational procedure to be performed for a software update. The operation to be performed by the user includes, for example, selection of software to be updated, selection of a function to be updated, ejection of the CD-ROM 201, and
25 the like.

The DVD reproduction apparatus 200 includes an MPEG decoder IC (Integrated Circuit) 202, a transmission signal conversion IC 203, a microprocessor 204, and a memory 205.

The television receiver 300 includes a reception signal
5 conversion IC 301, a video processor 302, an audio DAC (Digital-Analog Converter) circuit 303, a display 304, a speaker 305, a microprocessor 306, a memory 307, a microprocessor 308, a memory 309, and an expansion function unit 310.

10 The DVD reproduction apparatus 200 and the television receiver 300 are connected via an HDMI interface cable CA in conformity with the digital interface standard referred to as HDMI (High-Definition Multimedia Interface) standard. The HDMI interface cable CA includes data lines D1, D2, D3, a pixel
15 clock line CK, and a control line CT.

According to the HDMI standard, it is possible to transmit video data while transmitting supplemental data during blanking intervals (vertical blanking intervals and horizontal blanking intervals) of the video data. The
20 supplemental data includes audio data (audio packets) and data packets referred to as InfoFrames (Information Frames). Utilizing these InfoFrames, it is possible to transmit a variety of information (additional data). In the embodiment, the update software is transmitted by utilizing the
25 InfoFrames. The aforementioned operational guide

information comprises pixel components (video data) and audio data included in the InfoFrames.

The update software is divided into a plurality of data packets, and the plurality of data packets are distributed
5 to be inserted into InfoFrames for a plurality of blanking intervals. The update software is rearranged based on the header added to each data packet for restoration.

The microprocessor 204 in the DVD reproduction apparatus 200 controls the transmission signal conversion IC
10 203.

At normal times, the MPEG decoder IC 202 decodes the data recorded in a DVD in MPEG form, and at the time of a software update, it decodes the data recorded in the CD-ROM 201 in MPEG form (i.e., update software). The transmission signal
15 conversion IC 203 separates the data decoded by the MPEG decoder IC 202 into pixel component data (video data) and supplemental data, outputs a pixel clock to the pixel clock line CK, and outputs the pixel component data and the supplemental data to the data lines D1, D2, D3 in
20 synchronization with the pixel clock.

The data line D1 transfers the pixel component B (blue video data) and the supplemental data to the television receiver 300 in synchronization with the pixel clock on the pixel clock line CK. The data line D2 transfers the pixel
25 component G (green video data) and the supplemental data to

the television receiver 300 in synchronization with the pixel clock on the pixel clock line CK. The data line D3 transfers the pixel component R (red video data) and the supplemental data to the television receiver 300 in synchronization with
5 the pixel clock.

The reception signal conversion IC 301 in the television receiver 300 receives the pixel components and the supplemental data transferred via the data lines D1, D2, D3 in synchronization with the pixel clock on the pixel clock
10 line CK. The video processor 302 extracts a video signal from the pixel components received by the reception signal conversion IC 301, and causes the display 304 to display an image based on the video signal. The audio DAC circuit 303 converts the audio data included in the supplemental data
15 received by the reception signal conversion IC 301 to an analog audio signal, and causes the speaker 305 to output a sound based on the audio signal.

The microprocessor 306 controls the video processor 302 and the audio DAC circuit 303. The memory 307 stores software
20 for the microprocessor 306. The microprocessor 308 controls the expansion function unit 310. The memory 309 stores software for the microprocessor 308. The expansion function unit 310 implements functions available in addition to the inherent functions of the television receiver 300, i.e.,
25 displaying images and outputting sounds. For example, the

expansion function unit 310 includes such functions as recording of television broadcast and connection to the Internet.

Referring now to the flowcharts of Fig. 2 to Fig. 6, the operation of the software updating system in Fig. 1 is described.

Fig. 2 and Fig. 3 are flowcharts each showing processing by the microprocessor 204 in the DVD reproduction apparatus 200 in Fig. 1.

10 The CD-ROM 201 has recorded therein the update software and the operational guide information in MPEG form. The update software includes ID (identification) information representing for which of the microprocessors 204, 306, 308 the software should be updated.

15 When the user inserts the CD-ROM 201 into a CD-ROM drive (not shown) of the DVD reproduction apparatus 200, the MPEG decoder IC 202 decodes the data read from the CD-ROM 201, and feeds the decoded data to the transmission signal conversion IC 203. The transmission signal conversion IC 203 separates
20 the decoded data into pixel components and supplemental data.

The microprocessor 204 first reads InfoFrames from the supplemental data obtained from the MPEG decoder IC 202 (Step 1). Then, the microprocessor 204 determines whether or not update software is included in the read InfoFrames (Step S2).

When the update software is included in the InfoFrames, the microprocessor 204 instructs the microprocessor 306 in the television receiver 300 via the control line CT to display and output a visual and audio operational guide (Step S3),
5 and transmits operational guide information to the television receiver 300 as the pixel components and the supplemental data using the transmission signal conversion IC 203 (Step 4). This causes the display 304 in the television receiver 300 to display an image showing an operational procedure, and the
10 speaker 305 to output a sound showing the operational procedure.

Further, the microprocessor 204 determines whether or not the ID information in the InfoFrames read from the transmission signal conversion IC 203 indicates a software
15 update for the microprocessor 204 (Step S5).

When the ID information indicates a software update for the microprocessor 204, the microprocessor 204 rewrites the software stored in the memory 205 with the update software in the InfoFrames to update the software in the memory 205
20 (Step S6), to complete the processing.

When at Step S5, the ID information does not indicate a software update for the microprocessor 204, the microprocessor 204 instructs the microprocessor 306 in the television receiver 300 via the control line CT to update the

software for the microprocessor 306 or the microprocessor 308 based on the ID information (Step S7).

After that, the microprocessor 204 determines whether or not the microprocessor 306 is capable of receiving data (Step S8). When the microprocessor 306 is capable of receiving data, the microprocessor 204 transmits the update software for the microprocessor 306 or 308 to the television receiver 300 via the data lines D1, D2, D3 using the transmission signal conversion IC 203 (Step S9).

Then, the microprocessor 204 determines whether or not a reception completion notice has been received from the microprocessor 306 in the television receiver 300 via the control line CT (Step S10). The microprocessor 204 continues the processing at Step S9 until it receives the reception completion notice from the microprocessor 204. Upon reception of the reception completion notice from the microprocessor 204, the microprocessor 204 completes the processing.

If, at Step S2, the update software is not included in the InfoFrames, the microprocessor 204 completes the processing.

Fig. 4 and Fig. 5 are flowcharts each showing processing by the microprocessor 306 in the television receiver 300 in Fig. 1.

The microprocessor 306 in the television receiver 300 monitors the control line CT (Step S21) to determine whether or not it has been instructed via the control line CT to display and output the operational guide (Step S22). When
5 instructed to display and output the operational guide, the microprocessor 306 receives the operational guide information via the data lines D1, D2, D3 using the reception signal conversion IC 301 (Step S23).

Then, the microprocessor 306 determines whether or not
10 a software update has been indicated via the control line CT (Step S24).

Upon indication of a software update, the microprocessor 306 determines whether or not it is capable of receiving data (Step S25). When being capable of receiving
15 data, the microprocessor 306 informs the microprocessor 204 in the DVD reproduction apparatus 200 via the control line CT that it is capable of receiving data (Step S26).

The microprocessor 306 subsequently receives the update software via the data lines D1, D2, D3 using the reception
20 signal conversion IC 301 (Step S27). The microprocessor 306 determines whether or not the update software has been received from the DVD reproduction apparatus 200 (Step S28).

Upon reception of the update software, the microprocessor 306 transmits a reception completion notice

to the microprocessor 204 in the DVD reproduction apparatus 200 via the control line CT (Step S29).

Then, the microprocessor 306 determines whether or not the ID information in the InfoFrames indicates a software
5 update for the microprocessor 306 (Step S30).

When the ID information indicates a software update for the microprocessor 306, the microprocessor 306 rewrites the software stored in the memory 307 with the update software in the InfoFrames to update the software in the memory 307
10 (Step S31), to complete the processing.

When the ID information does not indicate a software update for the microprocessor 306, the microprocessor 306 instructs the microprocessor 308 to update the software via a serial bus line SB (Step S32). Further, the microprocessor
15 306 transmits the update software to the microprocessor 308 via the serial bus line SB (Step S33).

After that, the microprocessor 306 determines whether or not an update completion notice has been received from the microprocessor 308 via the serial bus line SB (Step S34). Upon
20 reception of the update completion notice from the microprocessor 308, the microprocessor 306 transmits the reception completion notice to the microprocessor 204 in the DVD reproduction apparatus 200 via the control line CT (Step S35), to complete the processing.

Fig. 6 is a flowchart showing processing by the microprocessor 308 in the television receiver 300 in Fig. 1.

The microprocessor 308 determines whether or not a software update has been indicated by the microprocessor 306 via the serial bus line SB (Step S41). Upon indication of a software update by the microprocessor 306, the microprocessor 308 receives the update software from the microprocessor 306 via the serial bus line SB (Step S42).

Then, the microprocessor 308 rewrites the software stored in the memory 309 with the update software received from the microprocessor 306 to update the software in the memory 309 (Step S43).

After that, the microprocessor 308 transmits an update completion notice to the microprocessor 306 via the serial bus line SB (Step S44).

In this way, the update software recorded in the CD-ROM 201 in MPEG form is downloaded onto the microprocessor 204 in the DVD reproduction apparatus 200 or onto the microprocessor 306 or 308 in the television receiver 300 based on the ID information, and the software stored in the memory 205, 307 or 309 is updated.

As described above, the software updating system according to the embodiment enables the software for any of the microprocessor 204 in the DVD reproduction apparatus 200 and the microprocessors 306, 308 in the television receiver

300 to be easily updated simply by inserting the CD-ROM 201 that has recorded therein the update software in MPEG form into the DVD reproduction apparatus 200 connected to the television receiver 300. This eliminates the necessity for a service man to visit each user's house to update software using a special technique. This results in substantial reductions in the service cost.

Moreover, in the software updating system according to the embodiment, connection of the DVD reproduction apparatus 200 to the television receiver 300 via the data lines D1, D2, D3, the pixel clock line CK, and the control line CT in conformity with the HDMI standard enables transfer of the operational guide information comprising video data and audio data from the DVD reproduction apparatus 200 to the television receiver 300. This causes the television receiver 300 to visually and audibly output the operational guide for software updating. Accordingly, the user can easily perform the operation for software updating according to the video and audio operational guide.

In the embodiment, an indication as to which of the microprocessors requires a software update is provided by the microprocessor 204 in the DVD reproduction apparatus 200 to the microprocessor 306 in the television receiver 300 using the control line CT. However, the embodiment is not limited to this, and the indication as to which of the microprocessors

requires a software update may be provided to the microprocessor 306 using the InfoFrames included in the supplemental data that is transmitted by the DVD reproduction apparatus 200 to the television receiver 300.

5 In addition, the operational guide information comprising video data and audio data is transmitted from the DVD reproduction apparatus 200 to the television receiver 300. However, operational guide information comprising audio data or video data may be transmitted from the DVD
10 reproduction apparatus 200 to the television receiver 300. In this case, the television receiver 300 outputs an audio operational guide or a video operational guide only.

 Furthermore, in the embodiment, the DVD reproduction apparatus 200 outputs the pixel component B, pixel component
15 G, and pixel component R to the data lines D1, D2, D3, respectively. However, the DVD reproduction apparatus 200 may output luminance data and two pieces of color-difference data to the data lines D1, D2, D3, respectively.

 In the embodiment, the DVD reproduction apparatus 200
20 corresponds to a data reproduction apparatus, and the television receiver 300 corresponds to a video display apparatus and a television receiver. The transmission signal conversion IC 203 corresponds to a transmitter, the microprocessor 204 corresponds to a controller, and the
25 memory 205 corresponds to a storage device. The reception

signal conversion IC 301 corresponds to a receiver, the video processor 302 and the display 304 correspond to a video display unit, and the audio DAC circuit 303 and the speaker 305 correspond to an audio output unit. The microprocessor 5 306 corresponds to a processor, the memory 307 corresponds to a storage, the microprocessor 308 corresponds to another processor, and the memory 309 corresponds to another storage. The infoFrames corresponds to a third area.